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ABSTRACT OF THE DISCLOSURE

A circuit and a method capable of adjusting the external clock of a CPU are disclosed. When needed, the computer user can adjust the external clock by inputting an external-clock value (for example, 66 or 33) via the keyboard. The keyboard controller feeds the control signal, corresponding to the external-clock value, into the external-clock storage device to store the external-clock value. Next, the south bridge circuit shuts down the computer system. And the wake-up circuit wakes up the south bridge circuit in a wake-up time (for example, one second) to reboot the computer system. Afterward the external-clock storage device feeds the external-clock value into the clock generator and the clock generator provides the CPU with the external clock according to the external-clock value.

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